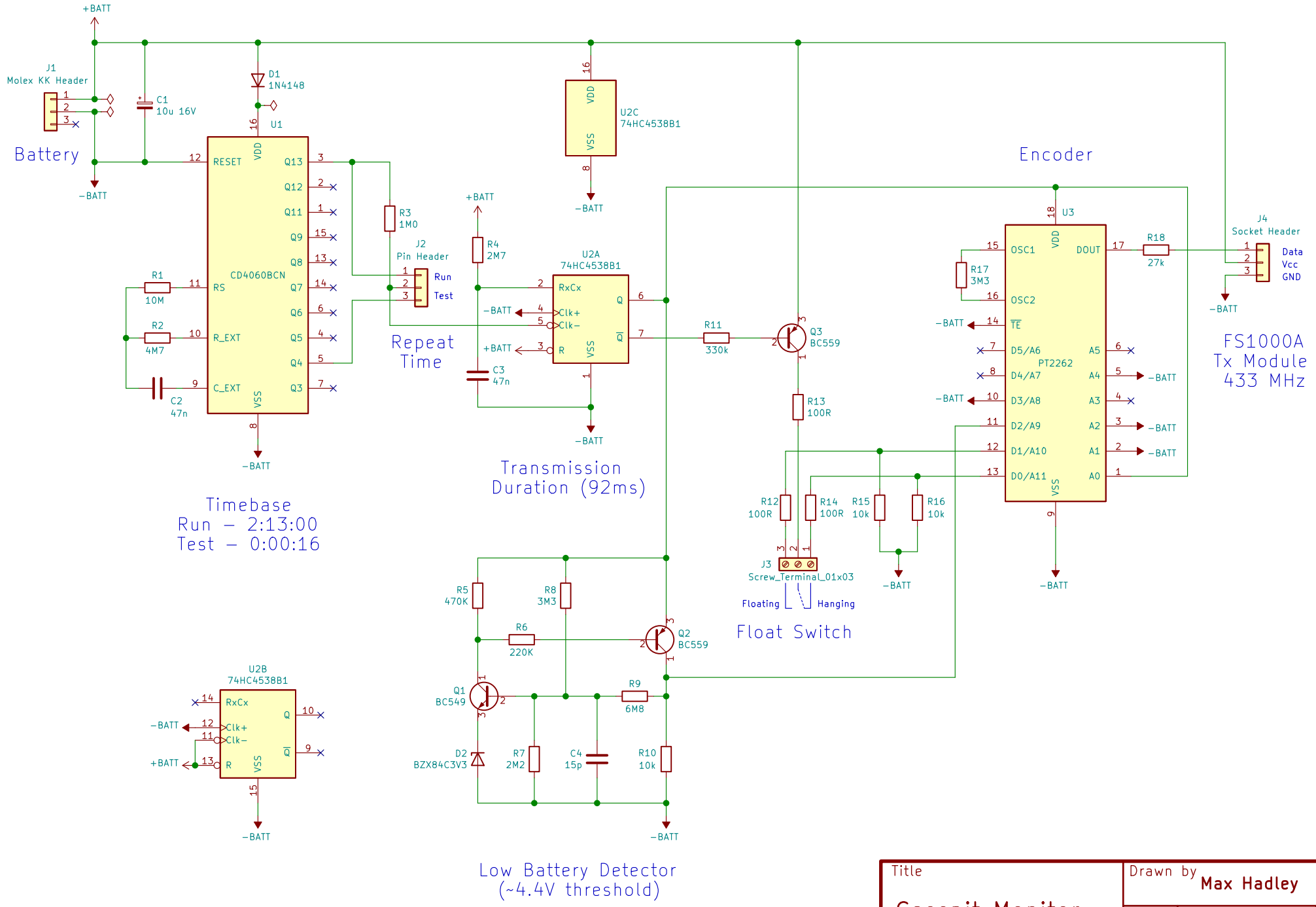
+BATT
J1
Molex KK Header
Battery
C1
10u 16V
D1
1N4148
U1
CD4060BCN
RESET
VDD
VSS
R_EXT
C_EXT
RS
Q13
Q12
Q11
Q9
Q8
Q7
Q6
Q5
Q4
Q3
R1
10M
R2
4M7
C2
47n
R3
1M0
J2
Pin Header
Run
Test
Repeat
Time
Timebase
Run – 2:13:00
Test – 0:00:16
U2C
74HC4538B1
U2A
74HC4538B1
RxCx
Clk+
Clk–
R
Q
Q!
R4
2M7
C3
47n
Transmission
Duration (92ms)
R11
330k
Q3
BC559
R13
100R
Encoder
U3
PT2262
OSC1
OSC2
TE
DOUT
D5/A6
D4/A7
D3/A8
D2/A9
D1/A10
D0/A11
A5
A4
A3
A2
A1
A0
R17
3M3
R18
27k
J4
Socket Header
Data
Vcc
GND
FS1000A
Tx Module
433 MHz
R12
100R
R14
100R
R15
10k
R16
10k
J3
Screw_Terminal_01x03
Floating
Hanging
Float Switch
R5
470K
R8
3M3
R6
220K
Q2
BC559
Q1
BC549
R9
6M8
D2
BZX84C3V3
R7
2M2
C4
15p
R10
10k
U2B
74HC4538B1
Low Battery Detector
(~4.4V threshold)
Title
Cesspit Monitor
Drawn by
Max Hadley
Rev
2
Date of issue
2021-02-24
Sheet
1/1