
TASK

All of HP's current logic analyzers and emulators can inverse assemble captured data. To avoid re-writing an inverse assembler for each different instrument, Hewlett-Packard has defined an Inverse Assembly Language that is compatible among all of the following instruments at the source code level:

- HP 64XXX Emulation
- HP 64620 State Analysis
- HP 1630A/D/G Logic Analyzer
- HP 1631A/D Logic Analyzer
- HP 1650A/B and HP 1651A/B Logic Analyzer
- HP 16510A/B and HP 16511B Logic Analyzer

Inverse assembler source code written to work on one of the machines listed above can be used in another machine, often with only minor changes.

The TASK communication variable is used to identify what kind of machine the inverse assembler is operating in. By using the TASK variable to identify which machine is being used, it may be possible to write a single inverse assembler that works properly in all of the above machines.

TASK will have the following values, depending on which HP instrument the inverse assembler is running in:

Value	Environment
0	Emulation--display memory mnemonic
1	Emulation--display trace mnemonic
2	Emulation--display trace status mnemonic
3	HP 64620 State Analysis
4	HP 1630A/D and HP 1631A/D Logic Analyzers
5	HP 1630G, HP 1650A/B, HP 1651A/B, HP 16510A/B and HP 16511B Logic Analyzers

For the HP 10391B IAL Development Package, TASK will always be 5 or greater.