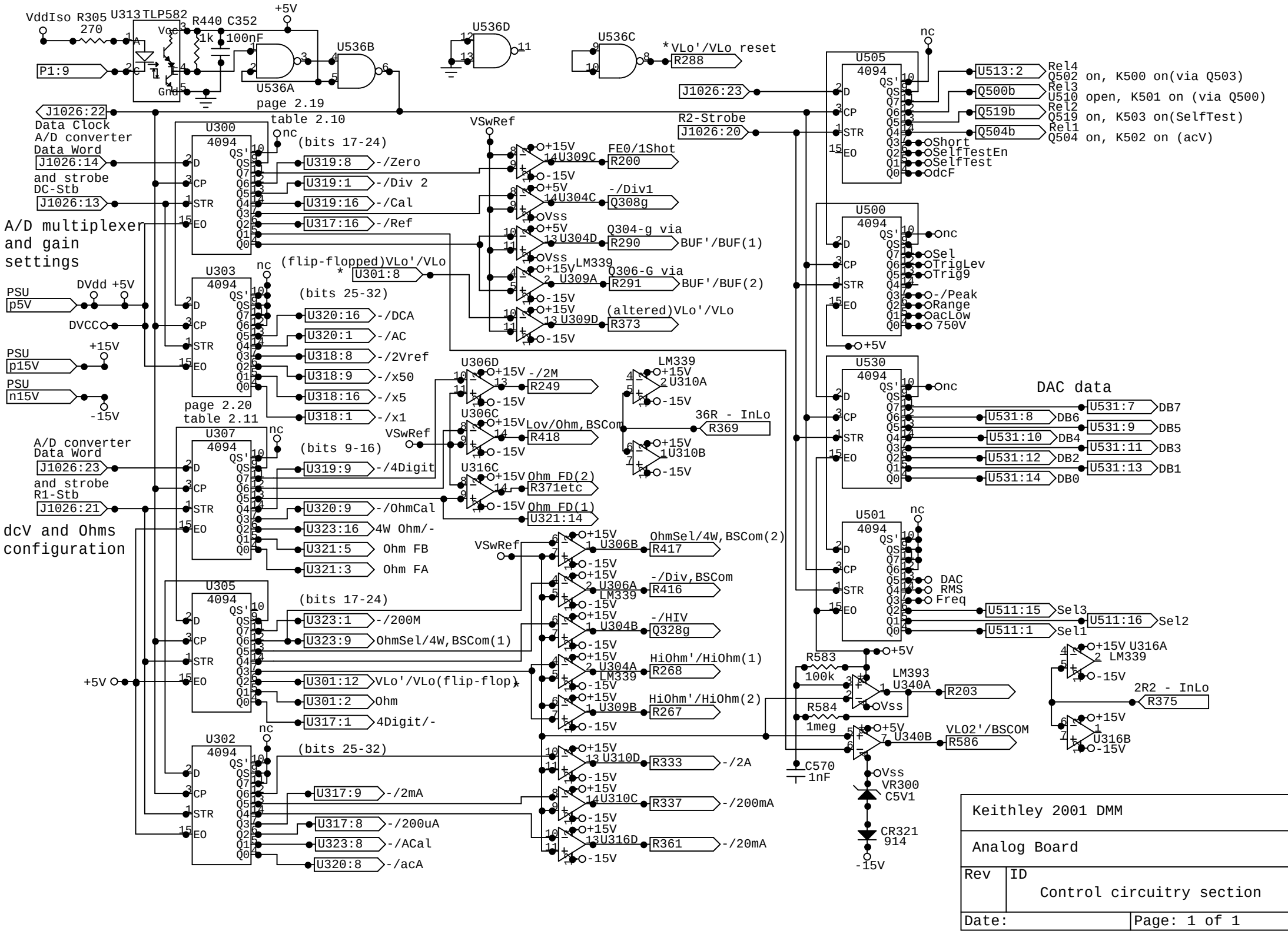
VddIso R305 270
U313 TLP582
R440 1k
C352 100nF
+5V
U536A
U536B
page 2.19
U536D
U536C
*VLo'/VLo reset
R288
P1:9
J1026:22
Data Clock
A/D converter
Data Word
J1026:14
and strobe
DC-Stb
J1026:13
A/D multiplexer
and gain
settings
table 2.10
U300
4094
(bits 17-24)
U319:8 -/Zero
U319:1 -/Div 2
U319:16 -/Cal
U317:16 -/Ref
VSwRef
U309C
FE0/1Shot
R200
U304C
-/Div1
Q308g
U304D
Q304-g via
R290
BUF'/BUF(1)
LM339
U309A
Q306-G via
R291
BUF'/BUF(2)
U309D
(altered)VLo'/VLo
R373
U303
(flip-flopped)VLo'/VLo
* U301:8
(bits 25-32)
U320:16 -/DCA
U320:1 -/AC
U318:8 -/2Vref
U318:9 -/x50
U318:16 -/x5
U318:1 -/x1
PSU
p5V
DVdd +5V
DVCC
PSU
p15V
+15V
PSU
n15V
-15V
page 2.20
table 2.11
U307
4094
(bits 9-16)
U319:9 -/4Digit
U320:9 -/OhmCal
U323:16 4W Ohm/-
U321:5 Ohm FB
U321:3 Ohm FA
A/D converter
Data Word
J1026:23
and strobe
R1-Stb
J1026:21
dcV and Ohms
configuration
U306D
-/2M
R249
U306C
Lov/Ohm,BSCom
R418
U316C
Ohm FD(2)
R371etc
Ohm FD(1)
U321:14
LM339
U310A
36R - InLo
R369
U310B
U305
4094
(bits 17-24)
U323:1 -/200M
U323:9 OhmSel/4W,BSCom(1)
U301:12 VLo'/VLo(flip-flop)*
U301:2 Ohm
U317:1 4Digit/-
U306B
OhmSel/4W,BSCom(2)
R417
U306A
-/Div,BSCom
R416
U304B
-/HIV
Q328g
U304A
HiOhm'/HiOhm(1)
R268
U309B
HiOhm'/HiOhm(2)
R267
U302
4094
(bits 25-32)
U317:9 -/2mA
U317:8 -/200uA
U323:8 -/ACal
U320:8 -/acA
U310D
R333
-/2A
U310C
R337
-/200mA
U316D
R361
-/20mA
J1026:23
J1026:20
R2-Strobe
U505
4094
U513:2 Rel4 Q502 on, K500 on(via Q503)
Q500b Rel3 U510 open, K501 on (via Q500)
Q519b Rel2 Q519 on, K503 on(SelfTest)
Q504b Rel1 Q504 on, K502 on (acV)
Short
SelfTestEn
SelfTest
dcF
U500
4094
Sel
TrigLev
Trig9
-/Peak
Range
acLow
750V
U530
4094
DAC data
U531:8 DB6
U531:7 DB7
U531:9 DB5
U531:10 DB4
U531:11 DB3
U531:12 DB2
U531:13 DB1
U531:14 DB0
U501
4094
DAC
RMS
Freq
U511:15 Sel3
U511:16 Sel2
U511:1 Sel1
R583 100k
R584 1meg
LM393
U340A
U340B
R203
VLO2'/BSCOM
R586
C570 1nF
VR300
C5V1
CR321 914
U316A
LM339
2R2 - InLo
R375
U316B
Keithley 2001 DMM
Analog Board
Rev
ID
Control circuitry section
Date:
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