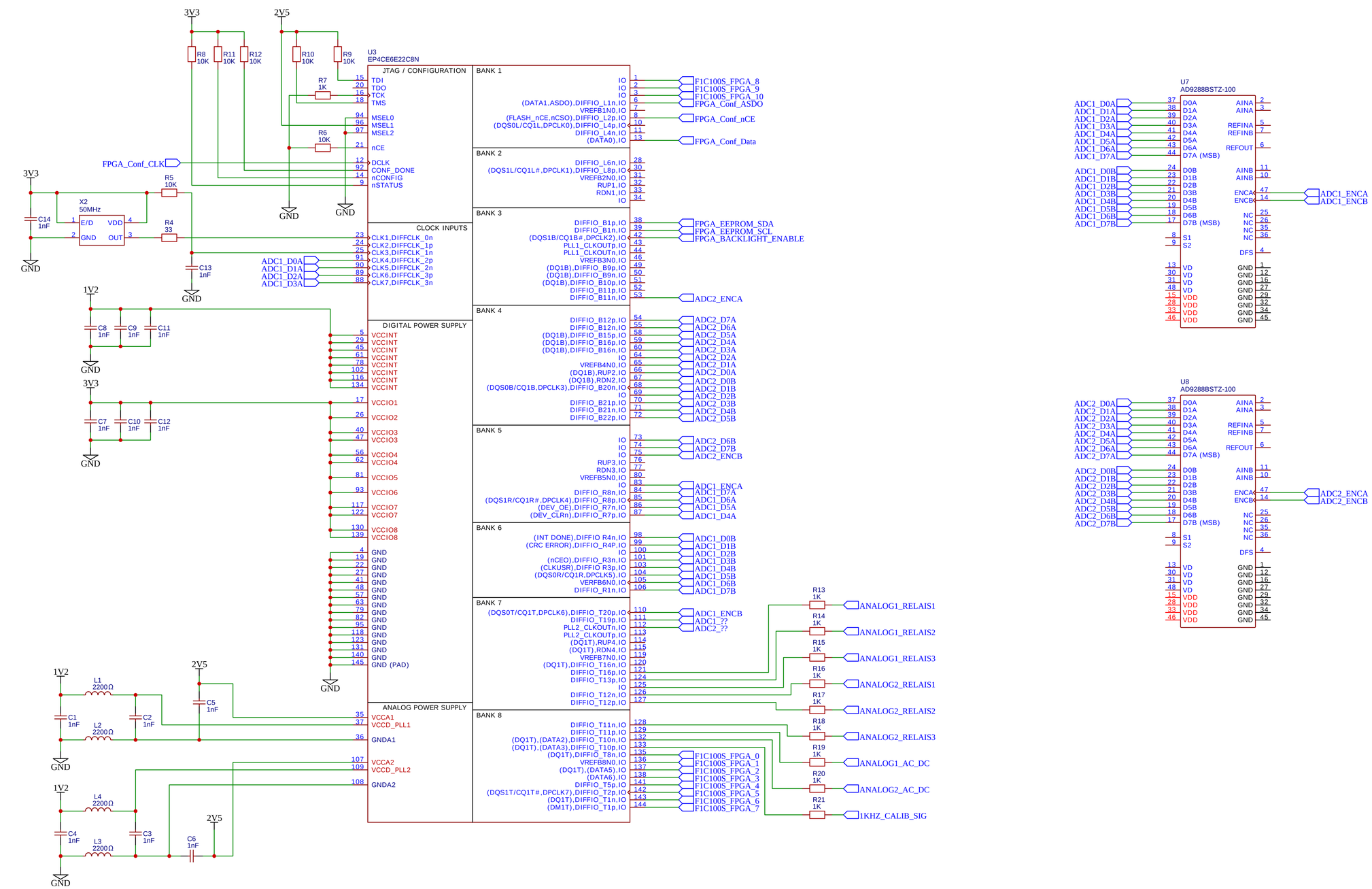
FPGA Configuration scheme is Active Serial 3V3 (MSEL inputs)



TITLE: Data acquisition	REV: 1.0
Company:	Sheet: 1/1
Date: 2021-03-23 Drawn By:	