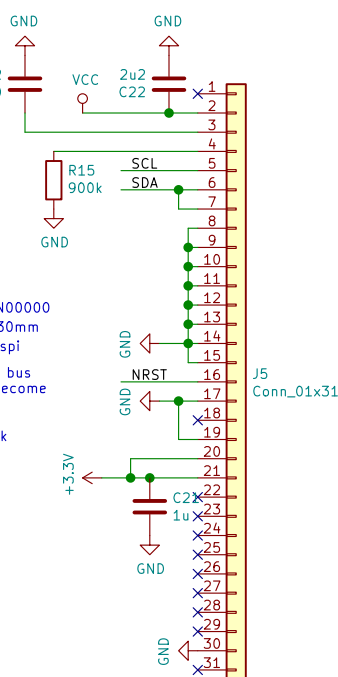
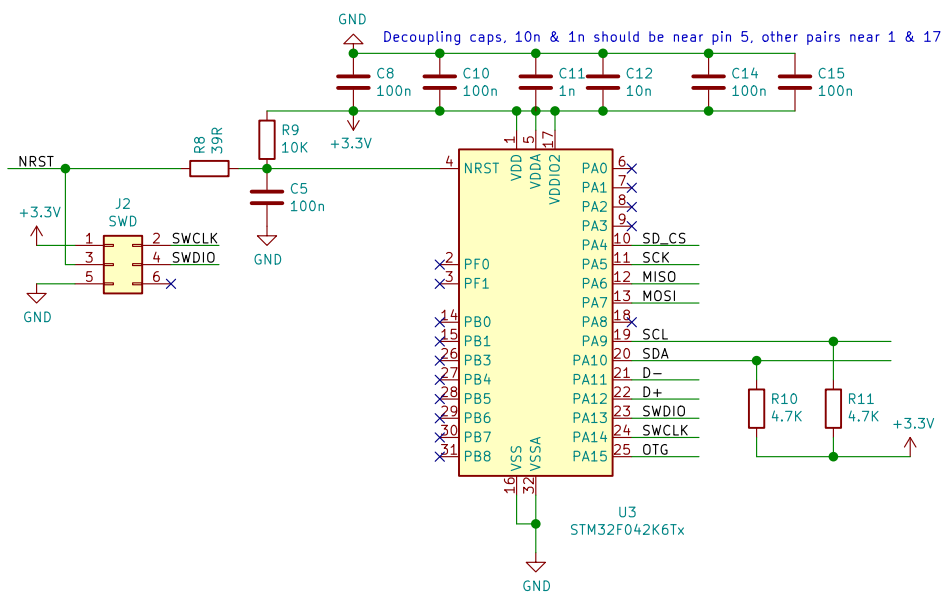
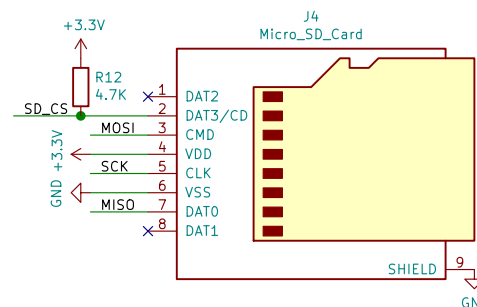
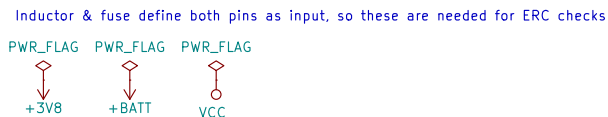
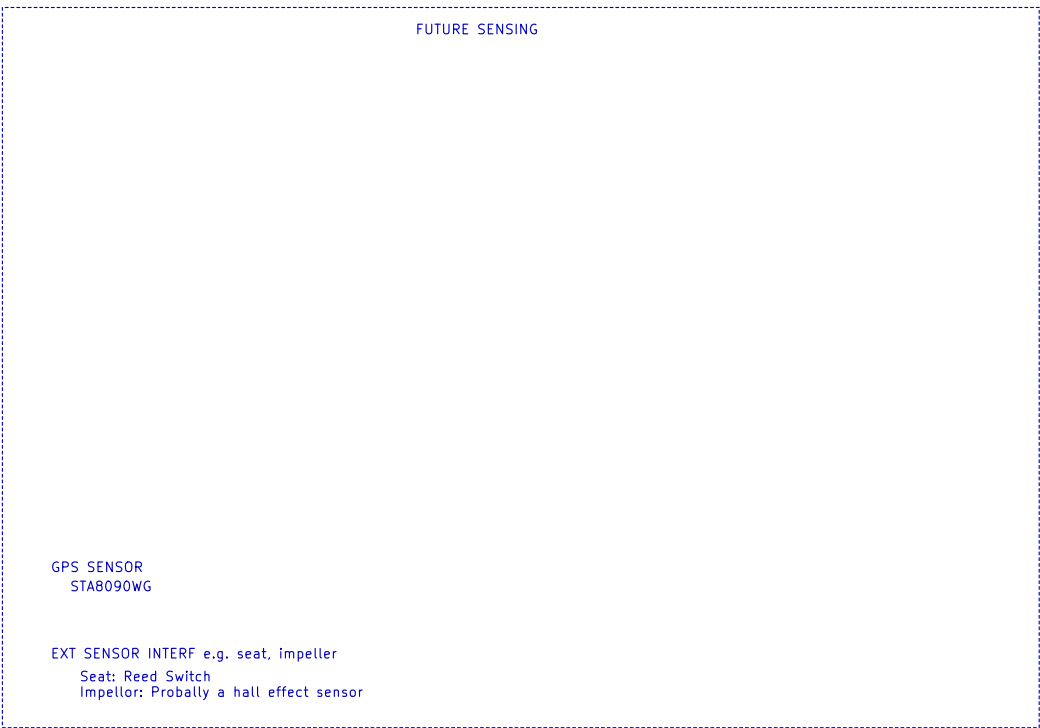


I'm not sure if a switch is needed on the usb Data lines which would be driven by DSEL, I think the spec means that the BQ will negotiate before USB host negotiation
Not sure if i need to use OTG or PMID I think not because i don't need boost mode or external device power
INT not connected but I might want it later



DISPLAY
OLED-128064H-LPP3N00000
128 x 64 approx 60 x 30mm
31pin 6800 mode, i2c, spi
Can change to different bus
if contention & speed become
a problem for I2C
Hirose FH55 should work
here 0.5mm pitch



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