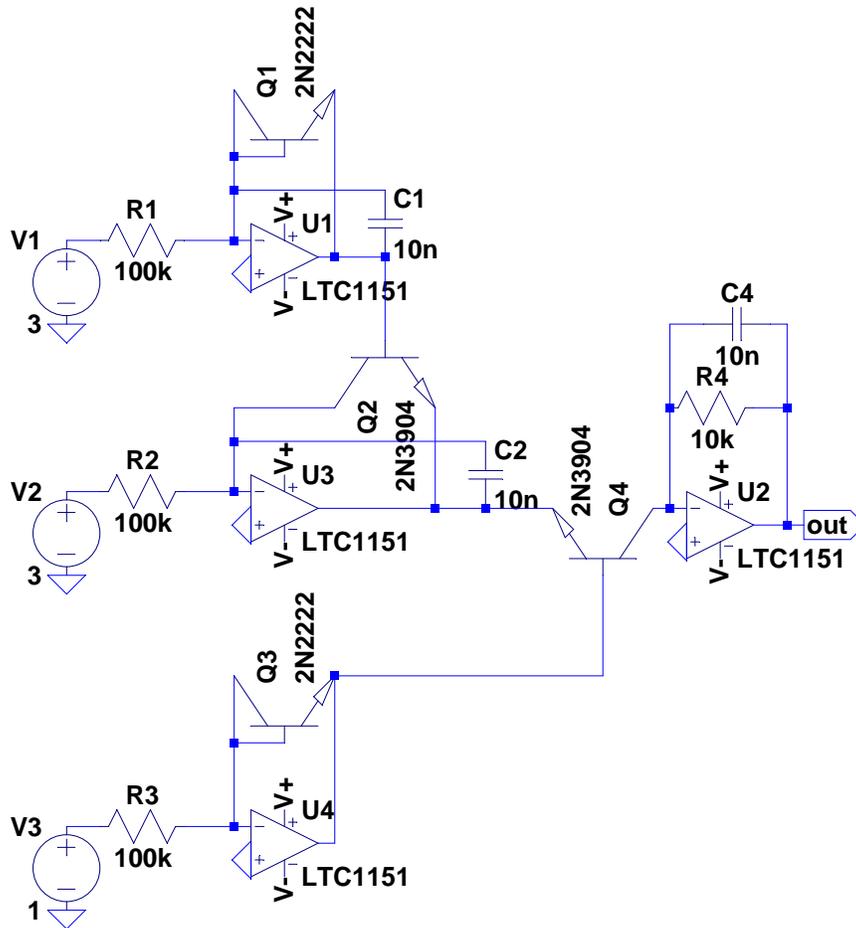


Make sure $R1=R2=R3=R4$ and that they are large to reduce the error caused by base current and self heating. This also results in a much larger input voltage range for a given output error. Q1/Q3 and Q2/Q4 must be matched to reduce error resulting from I_s variations.



.tran 50m startup