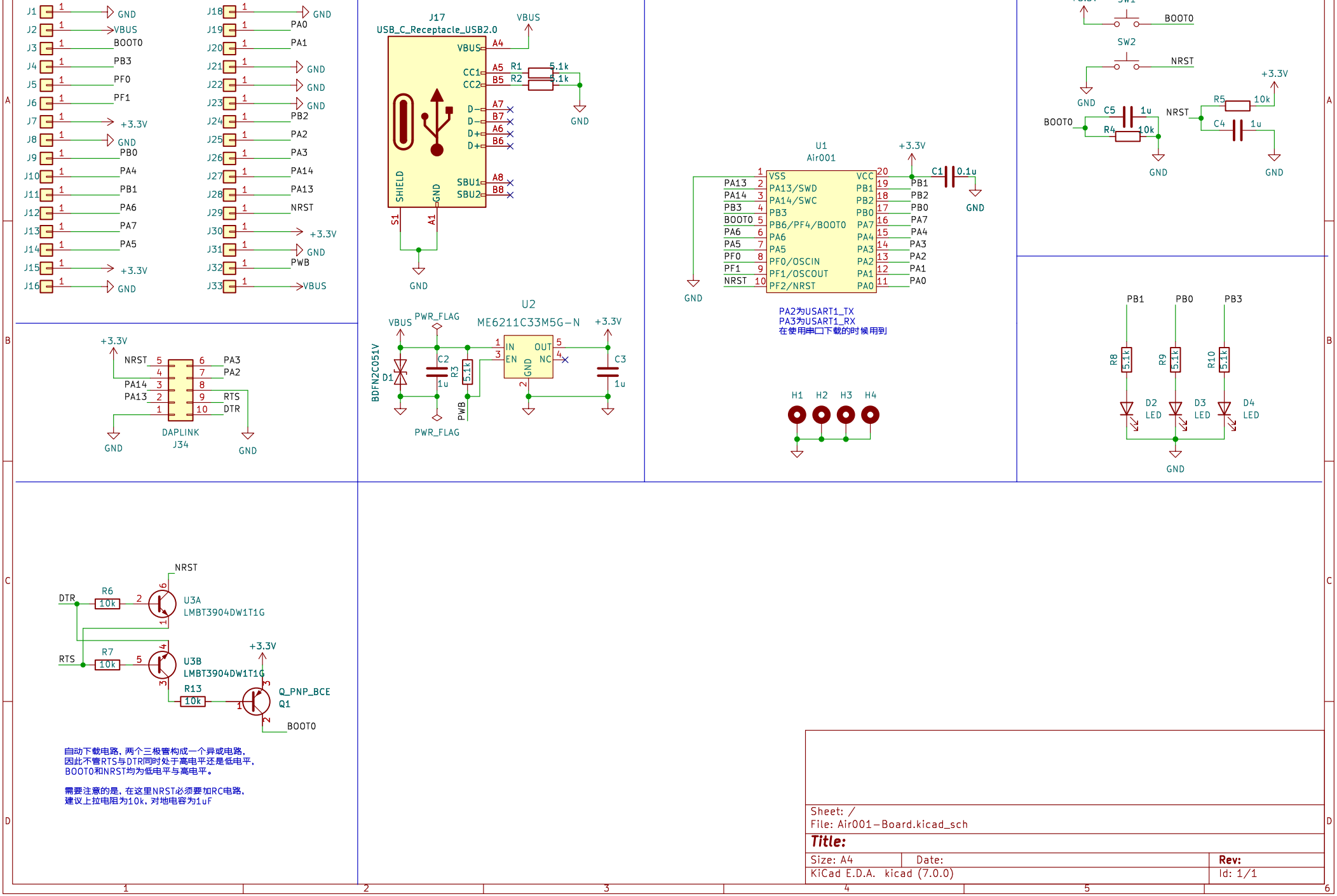
J1 GND
J2 VBUS
J3 BOOT0
J4 PB3
J5 PF0
J6 PF1
J7 +3.3V
J8 GND
J9 PB0
J10 PA4
J11 PB1
J12 PA6
J13 PA7
J14 PA5
J15 +3.3V
J16 GND
J18 GND
J19 PA0
J20 PA1
J21 GND
J22 GND
J23 GND
J24 PB2
J25 PA2
J26 PA3
J27 PA14
J28 PA13
J29 NRST
J30 +3.3V
J31 GND
J32 PWB
J33 VBUS
J17
USB_C_Receptacle_USB2.0
VBUS
CC1
CC2
D-
D-
D+
D+
SBU1
SBU2
SHIELD
GND
S1
A1
A4
A5
A6
A7
A8
B5
B6
B7
B8
R1 5.1k
R2 5.1k
U2
ME6211C33M5G-N
PWR_FLAG
BDFN2C051V
D1
C2 1u
R3 5.1k
IN
EN
GND
OUT
NC
C3 1u
U1
Air001
VSS
PA13/SWD
PA14/SWC
PB3
PB6/PF4/BOOT0
PA6
PA5
PF0/OSCIN
PF1/OSCOUT
PF2/NRST
VCC
PB1
PB2
PB0
PA7
PA4
PA3
PA2
PA1
PA0
C1 0.1u
PA2为USART1_TX
PA3为USART1_RX
在使用串口下载的时候用到
SW1
BOOT0
SW2
NRST
C5 1u
R4 10k
R5 10k
C4 1u
H1
H2
H3
H4
R8 5.1k
R9 5.1k
R10 5.1k
D2 LED
D3 LED
D4 LED
DAPLINK
J34
RTS
DTR
R6 10k
R7 10k
R13 10k
U3A
LMBT3904DW1T1G
U3B
LMBT3904DW1T1G
Q_PNP_BCE
Q1
自动下载电路，两个三极管构成一个异或电路，
因此不管RTS与DTR同时处于高电平还是低电平，
BOOT0和NRST均为低电平与高电平。
需要注意的是，在这里NRST必须要加RC电路，
建议上拉电阻为10k，对地电容为1uF
Sheet: /
File: Air001-Board.kicad_sch
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. kicad (7.0.0)
Id: 1/1