

Sheet: /ADC/
File: ADC.sch

Title:

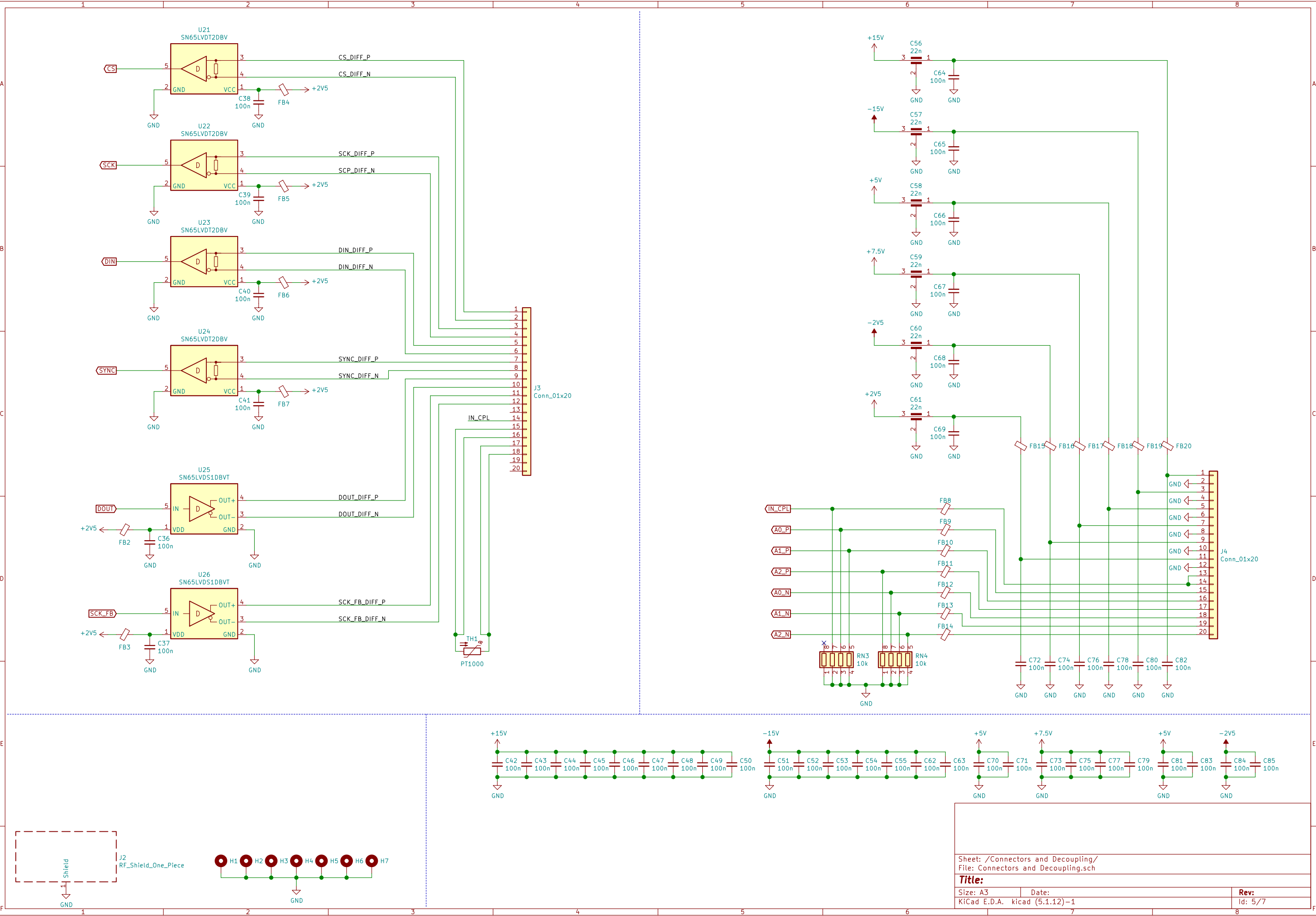
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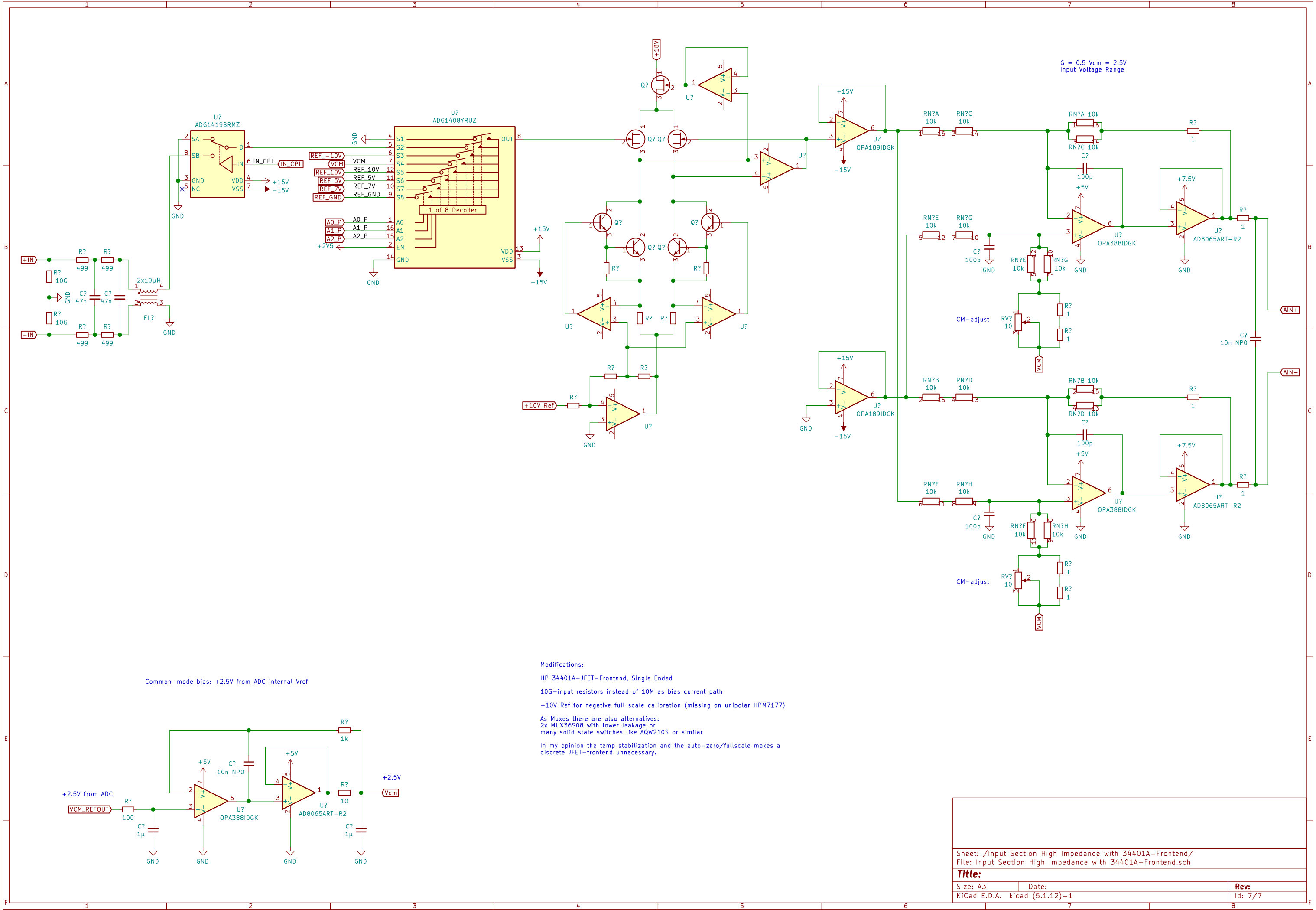
Date:

KiCad E.D.A. kicad (5.1.12)-1

Rev:

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Common-mode bias: +2.5V from ADC internal Vref

Modifications:

- HP 34401A-JFET-Frontend, Single Ended
- 10G-input resistors instead of 10M as bias current path
- 10V Ref for negative full scale calibration (missing on unipolar HPM7177)

As Muxes there are also alternatives:
2x MUX36S08 with lower leakage or
many solid state switches like AQW210S or similar

In my opinion the temp stabilization and the auto-zero/fullscale makes a discrete JFET-frontend unnecessary.