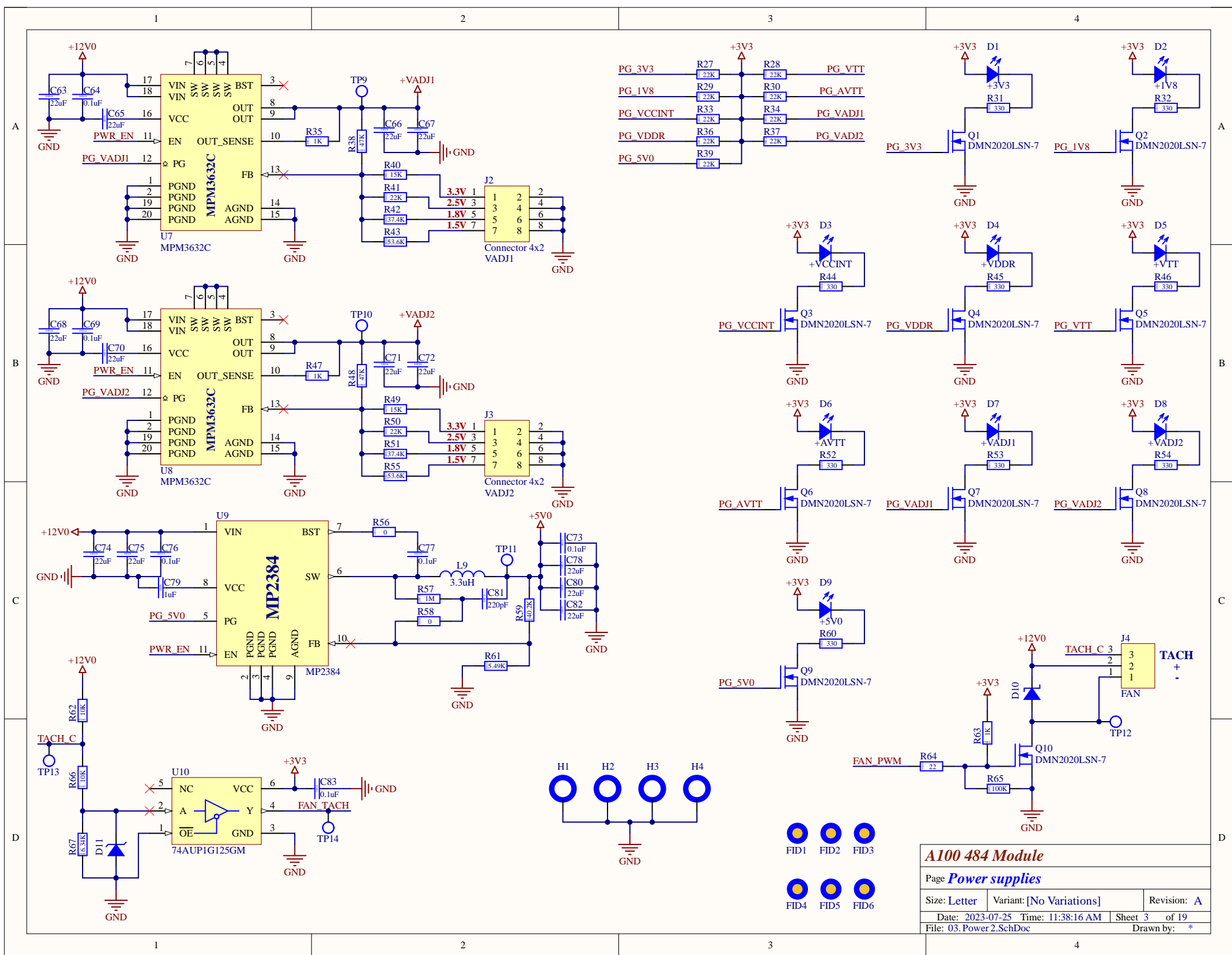


A100 484 Module

Page **Power supplies**

Size: Letter	Variant: [No Variations]	Revision: A
Date: 2023-07-25	Time: 11:38:16 AM	Sheet 2 of 19
File: 02_Power.SchDoc	Drawn by: *	



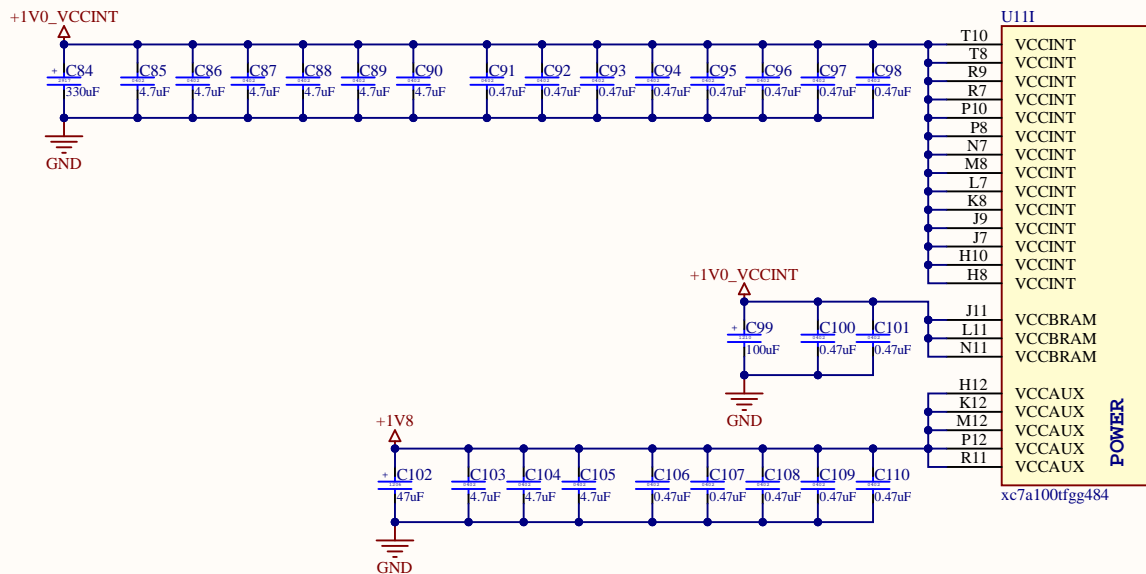
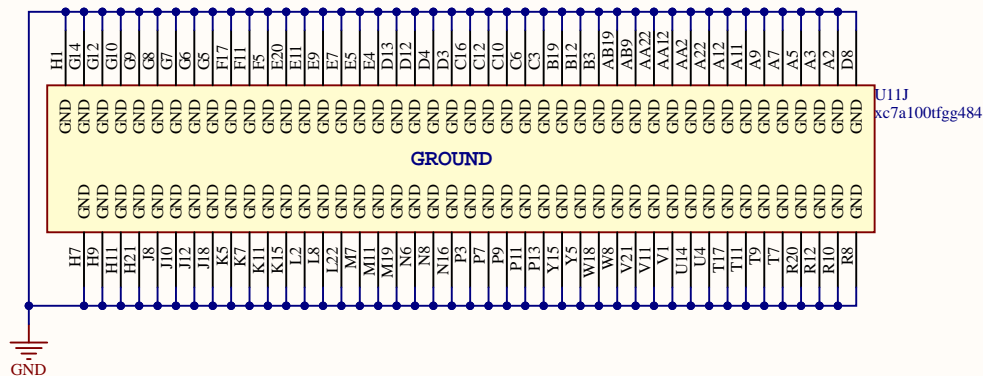


Table 2-2: Required PCB Capacitor Quantities per Device: Artix-7 Devices⁽¹⁾⁽²⁾ (Continued)

Package	Device	VCCINT						VCCBRAM				VCCAUX				VCCO Bank 0	VCCO all other Banks (per Bank)			
		680 μ F	330 μ F	100 μ F	47 μ F	4.7 μ F	0.47 μ F	100 μ F	47 μ F	4.7 μ F	0.47 μ F	47 μ F	4.7 μ F	0.47 μ F	47 μ F	47 μ F or 100 μ F ⁽³⁾	4.7 μ F	0.47 μ F		
FGG484	XC7A15T	0	0	1	0	2	2	0	1	0	1	1	2	5	1	1	2	4		
FGG484	XC7A35T	0	0	1	0	2	3	0	1	0	1	1	2	5	1	1	2	4		
FGG484	XC7A50T XQ7A50T	0	1	0	0	3	5	1	0	0	1	1	2	5	1	1	2	4		
FGG484	XC7A75T XA7A75T	0	1	0	0	4	6	1	0	0	2	1	3	5	1	1	2	4		
FGG484	XC7A100T XA7A100T XQ7A100T	0	1	0	0	6	8	1	0	0	2	1	3	5	1	1	2	4		



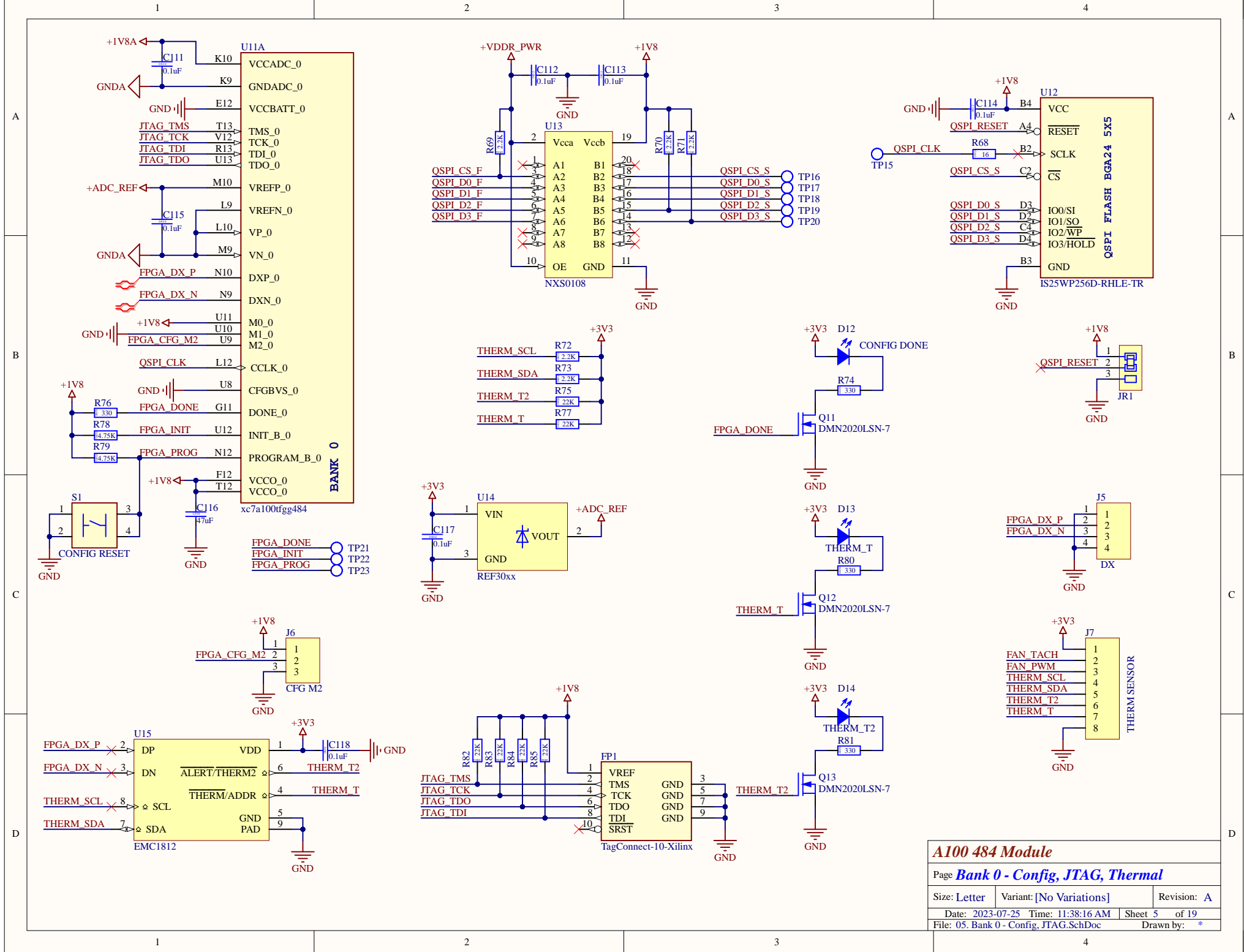
A100 484 Module

Page **FPGA Power and Ground**

Size: Letter Variant: [No Variations] Revision: A

Date: 2023-07-25 Time: 11:38:16 AM Sheet 4 of 19

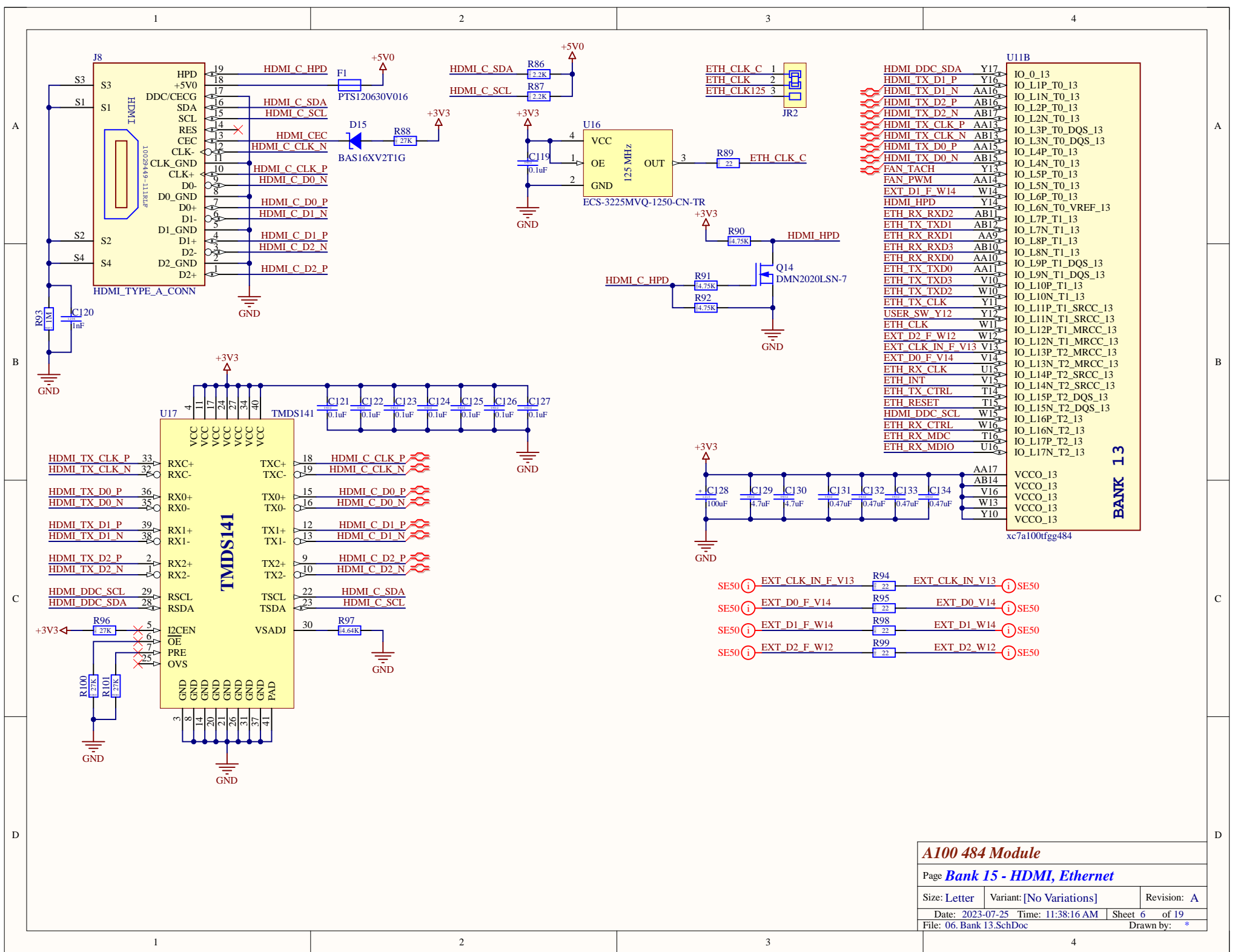
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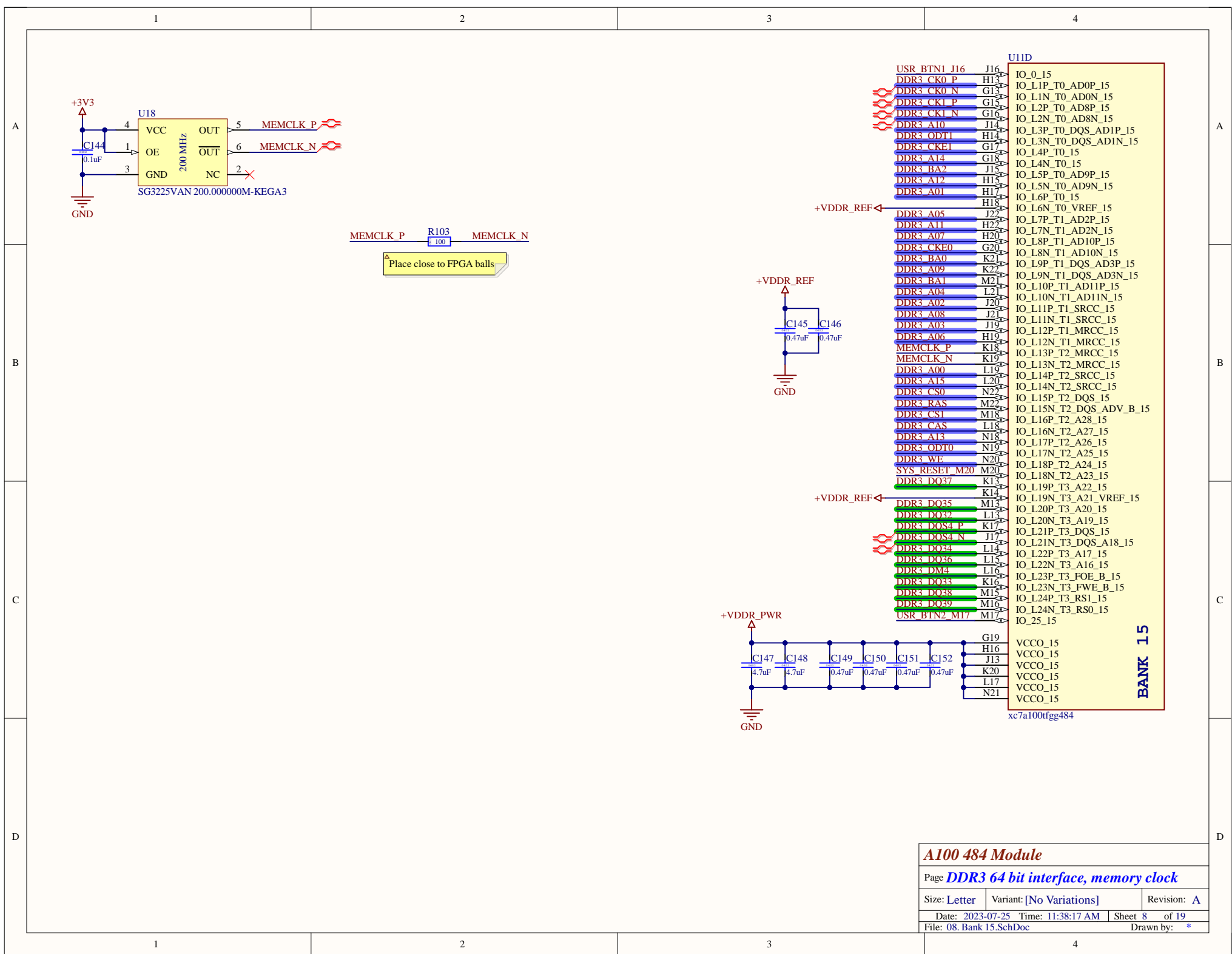


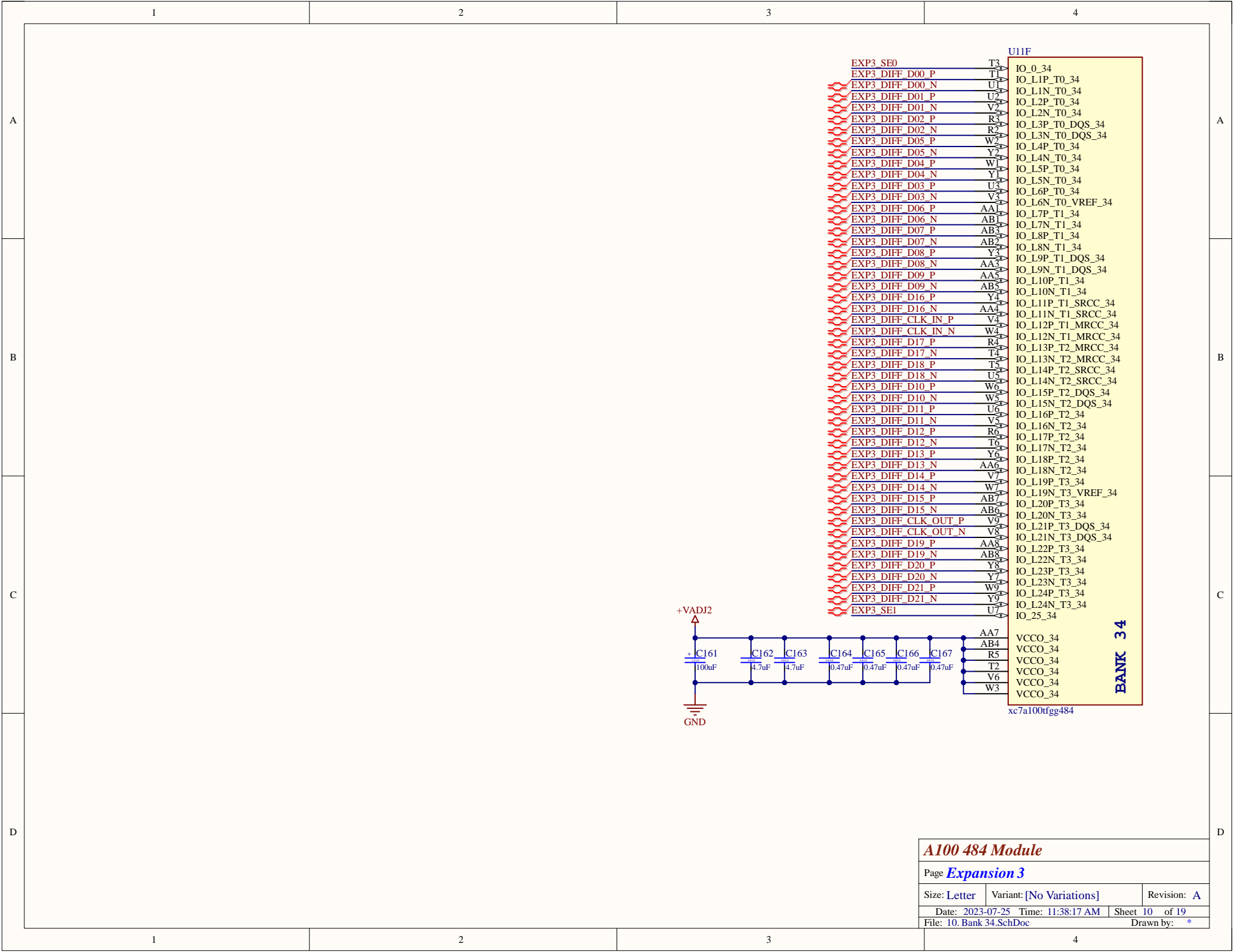
A100 484 Module

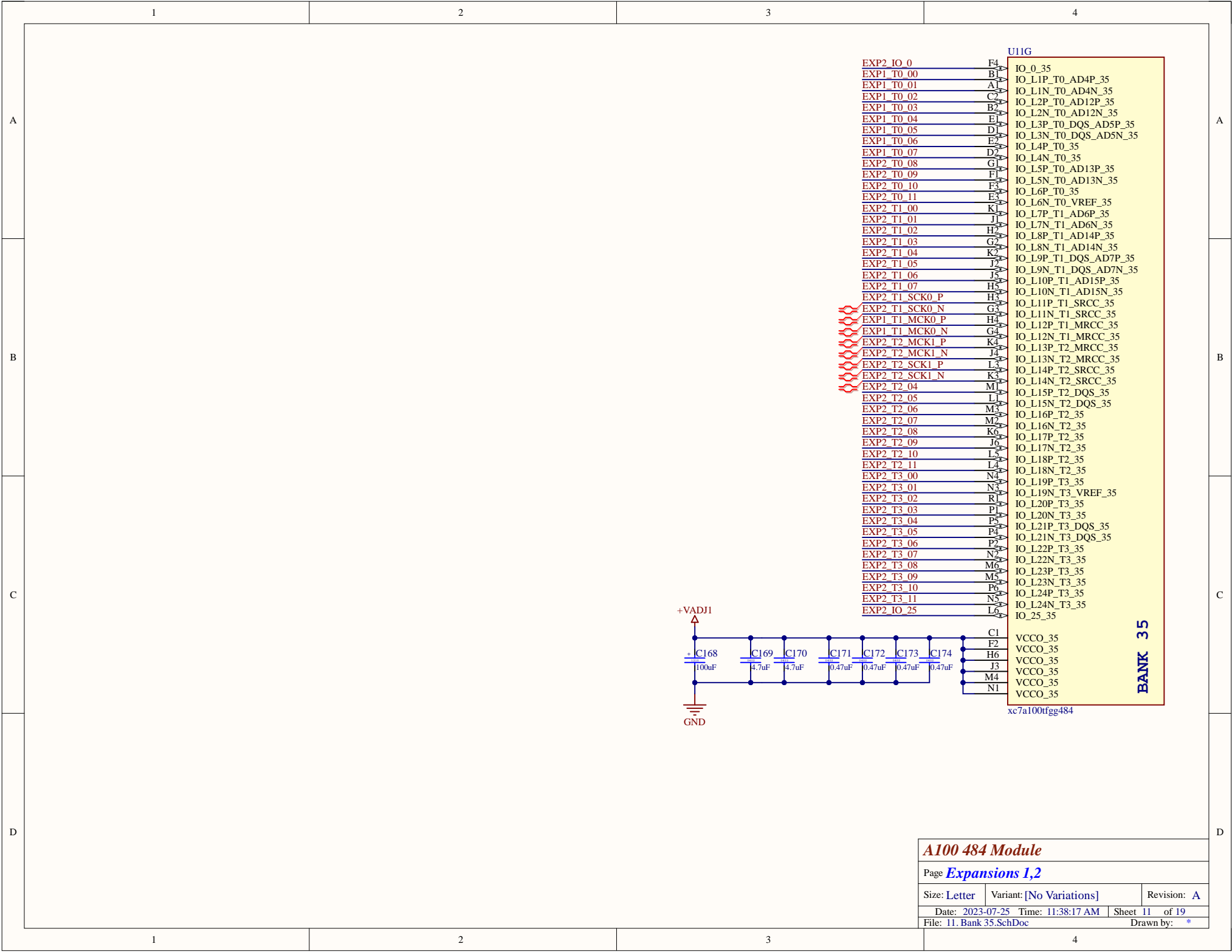
Page **Bank 0 - Config, JTAG, Thermal**

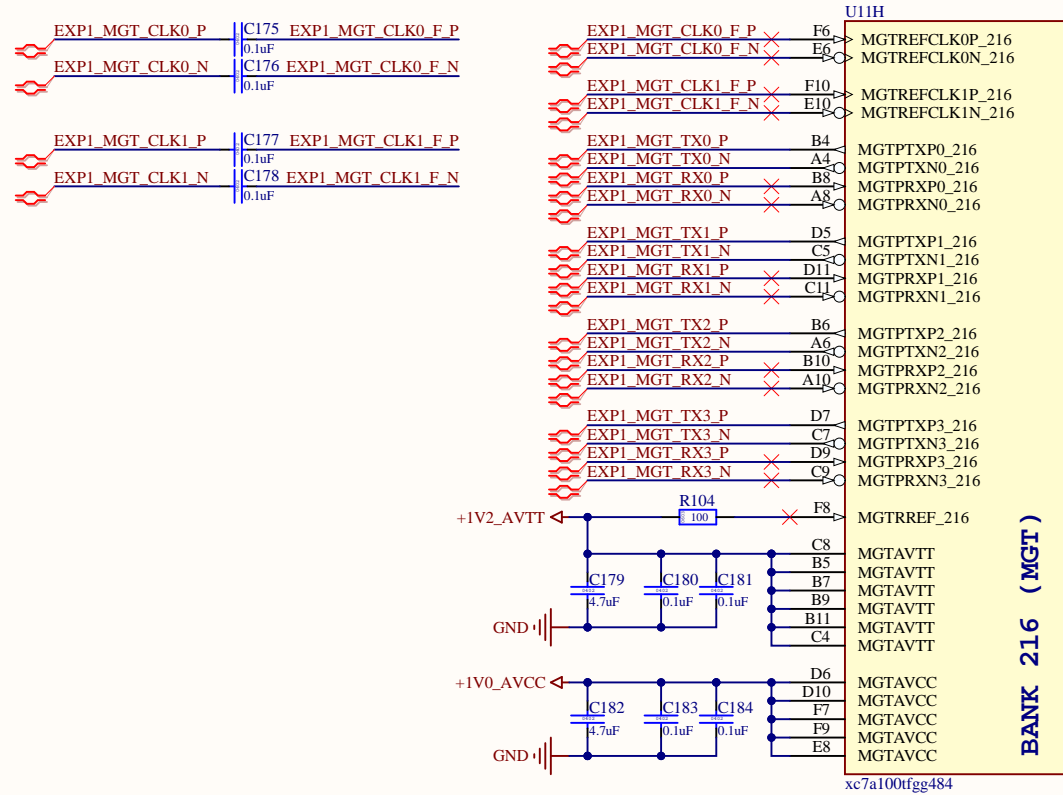
Size: Letter	Variant: [No Variations]	Revision: A
Date: 2023-07-25	Time: 11:38:16 AM	Sheet 5 of 19
File: 05_Bank 0 - Config, JTAG.SchDoc	Drawn by: *	











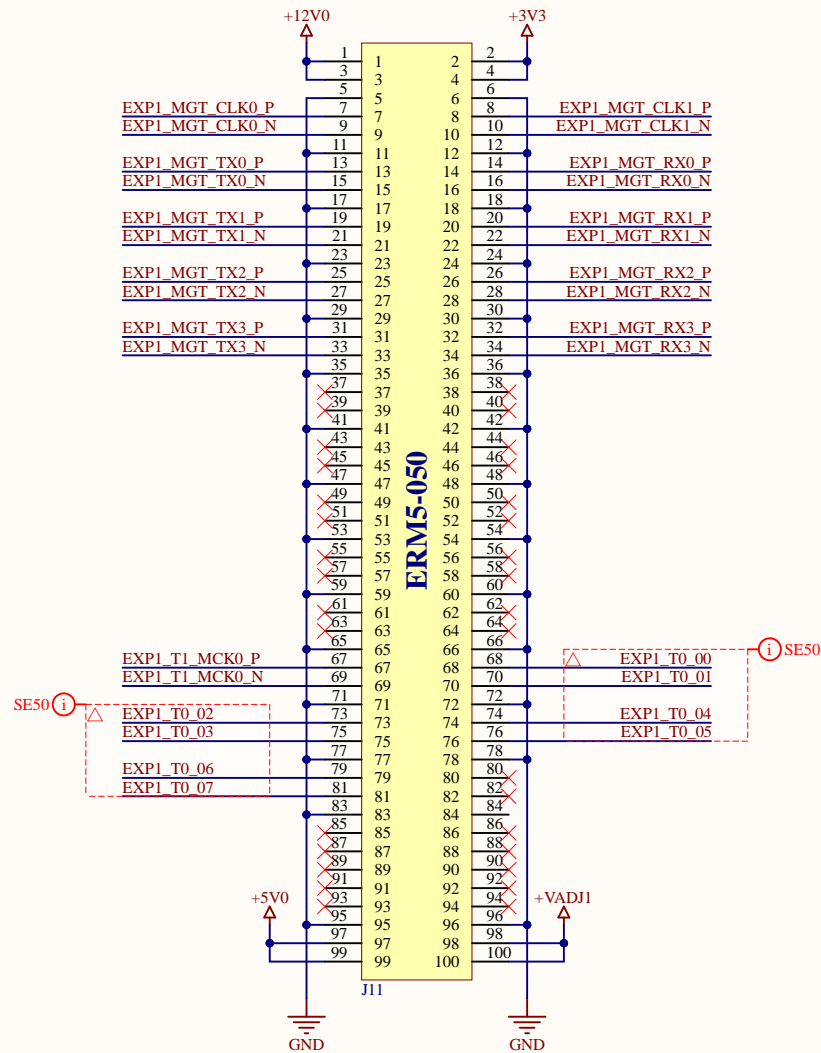
A100 484 Module

Page **Transceivers**

Size: Letter Variant: [No Variations] Revision: A

Date: 2023-07-25 Time: 11:38:17 AM Sheet 12 of 19

File: 12_Bank 216.SchDoc Drawn by: *

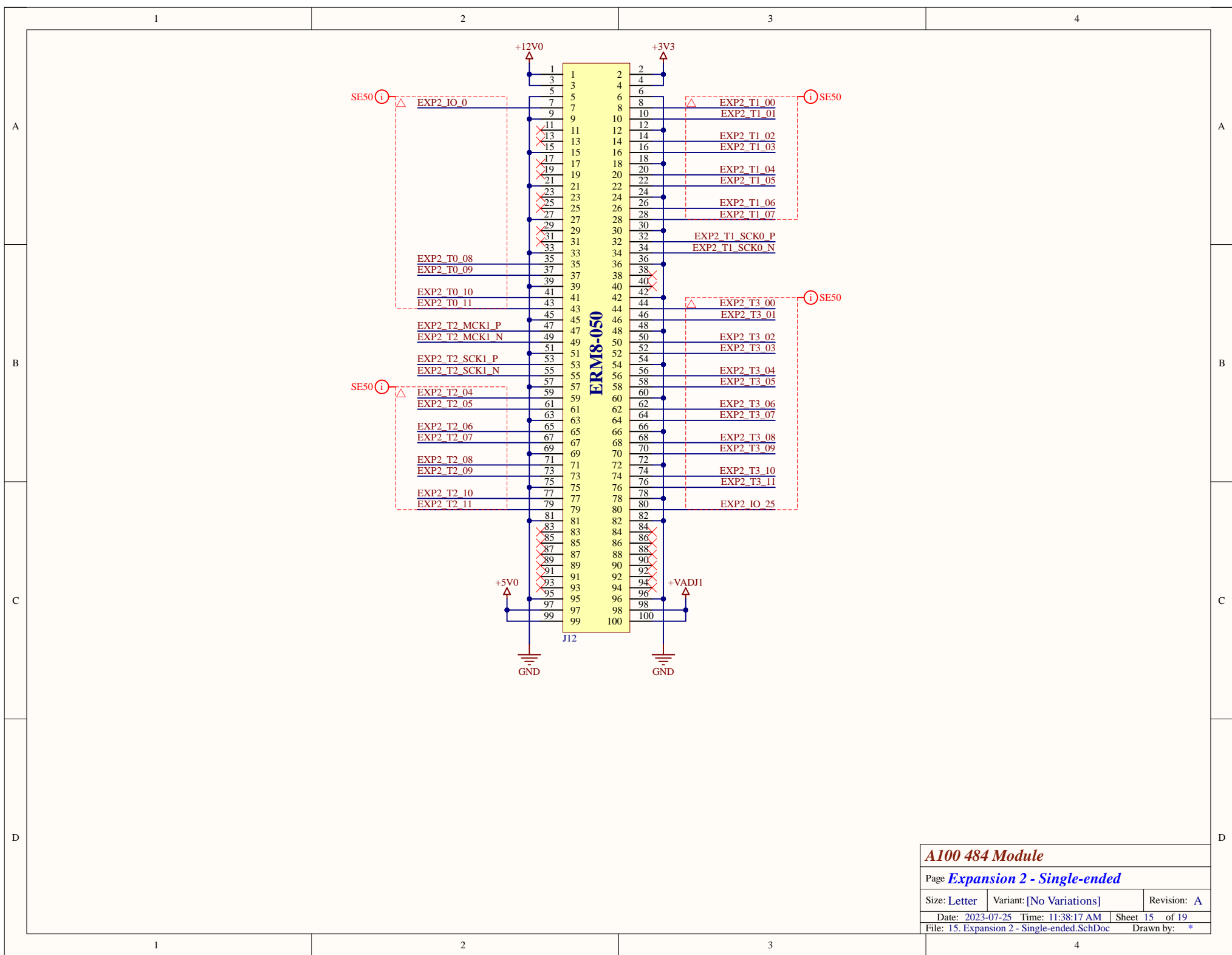


A100 484 Module

Page **Expansion 1 - MGT**

Size: Letter Variant: [No Variations] Revision: A

Date: 2023-07-25 Time: 11:38:17 AM Sheet 14 of 19
File: 14_Expansion 1 - MGT.SchDoc Drawn by: *

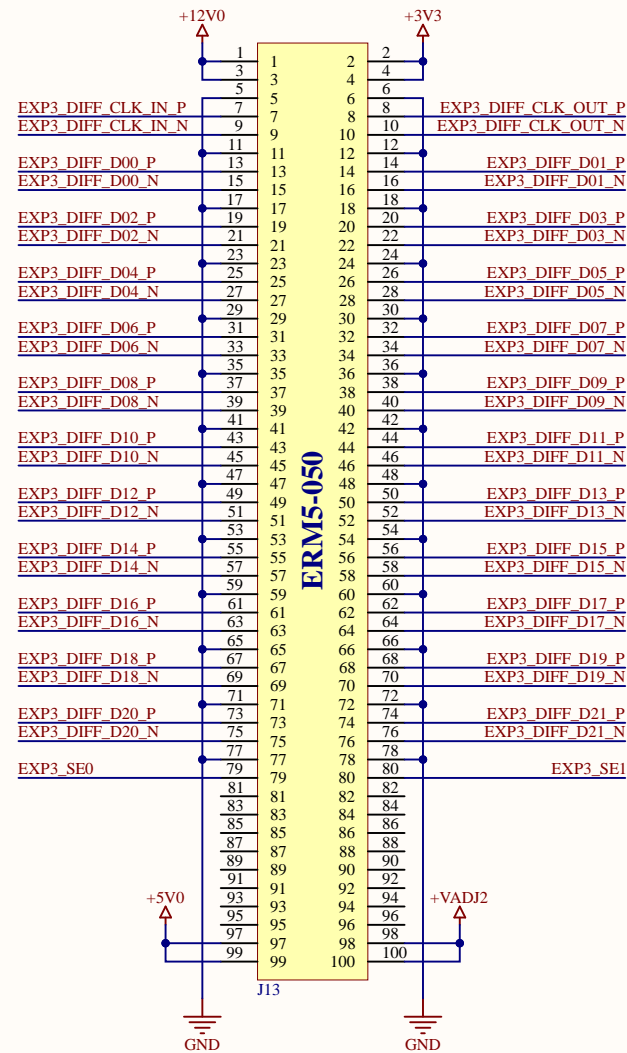


A

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D



A

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D

A100 484 Module			
Page Expansion 3 - Differential Pairs			
Size: Letter	Variant: [No Variations]	Revision: A	
Date: 2023-07-25		Time: 11:38:17 AM	Sheet 16 of 19
File: 16. Expansion 3 - Diff pairs.SchDoc		Drawn by: *	

