

PACMAN Project Status (03/17/2012 - 15:16:44)			
Project File:	Pacman.xise	Parser Errors:	No Errors
Module Name:	PACMAN	Implementation State:	Programming File Generated
Target Device:	xc3s1200e-4fg320	• Errors:	No Errors
Product Version:	ISE 14.7	• Warnings:	92 Warnings (92 new)
Design Goal:	Balanced	• Routing Results:	All Signals Completely Routed
Design Strategy:	Xilinx Default (unlocked)	• Timing Constraints:	All Constraints Met
Environment:	System Settings	• Final Timing Score:	0 (Timing Report)

Device Utilization Summary				[-]
Logic Utilization	Used	Available	Utilization	Note(s)
Number of Slice Flip Flops	633	17,344	3%	
Number of 4 input LUTs	2,458	17,344	14%	
Number of occupied Slices	1,482	8,672	17%	
Number of Slices containing only related logic	1,482	1,482	100%	
Number of Slices containing unrelated logic	0	1,482	0%	
Total Number of 4 input LUTs	2,611	17,344	15%	
Number used as logic	2,362			
Number used as a route-thru	153			
Number used for Dual Port RAMs	96			
Number of bonded IOBs	63	250	25%	
Number of RAMB16s	19	28	67%	
Number of BUFGMUXs	3	24	12%	
Number of DCMs	1	8	12%	
Average Fanout of Non-Clock Nets	3.71			

Performance Summary				[-]
Final Timing Score:	0 (Setup: 0, Hold: 0, Component Switching Limit: 0)	Pinout Data:	Pinout Report	
Routing Results:	All Signals Completely Routed	Clock Data:	Clock Report	
Timing Constraints:	All Constraints Met			

Detailed Reports						[-]
Report Name	Status	Generated	Errors	Warnings	Infos	
Synthesis Report	Current	Sat Mar 17 20:08:46 2012	0	76 Warnings (76 new)	19 Infos (19 new)	
Translation Report	Current	Sat Mar 17 20:08:54 2012	0	0	2 Infos (2 new)	
Map Report	Current	Sat Mar 17 20:09:02 2012	0	5 Warnings (5 new)	3 Infos (3 new)	
Place and Route Report	Current	Sat Mar 17 20:09:35 2012	0	7 Warnings (7 new)	0	
Power Report						
Post-PAR Static Timing Report	Current	Sat Mar 17 20:09:41 2012	0	0	5 Infos (5 new)	
Bitgen Report	Current	Sat Mar 17 20:09:57 2012	0	4 Warnings (4 new)	0	

Secondary Reports			[-]
Report Name	Status	Generated	
WebTalk Report	Current	Sat Mar 17 20:09:58 2012	
WebTalk Log File	Current	Sat Mar 17 20:10:02 2012	

Date Generated: 10/19/2019 - 14:17:07