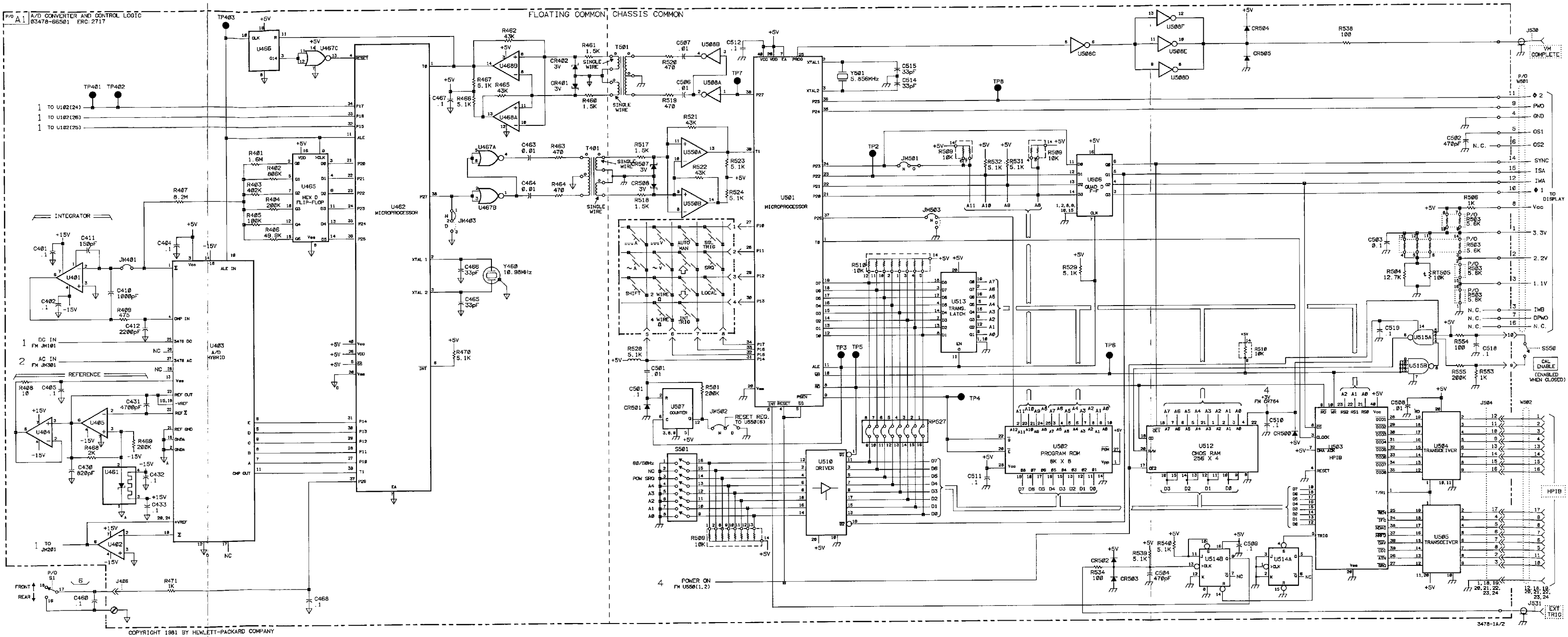

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Figure 7-G-5. A/D Converter and Control Logic